

Short Bio of Dr. Hasan Mahmood Aminul Islam

Dr. Hasan Mahmood Aminul Islam is an Assistant Professor in the Department of Computer Science and Engineering (CSE) at East West University in Dhaka, Bangladesh. He earned a Doctor of Science degree in Technology (CSE) from the School of Science at Aalto University, Finland, in 2018. Previously, he worked as a Specialist in System-on-Chip (SoC) Software at Nokia headquarters in Espoo, Finland, where he developed L1-Low System-on-Chip (SoC) software drivers for 5G technology from 2019 to 2022. During his doctoral studies, he played a key role in the EU-H2020 POINT Project, which focused on Future Internet Architecture. For his master's thesis, he collaborated with the Nomadic Laboratory at L.M. Ericsson in Finland, working closely with the IETF community on data channel protocol options and PeerConnection API for RTCWeb (e.g., **Google Meet / Microsoft Teams**). His research interests include data communications and distributed systems, Internet protocols, IoT protocols, Future Internet architecture, Information-Centric networking, and Delay-Tolerant Networking.

Short Bio of Dr. Anisur Rahman

Dr. Anisur Rahman received a B.S. and an M.S. from Middle East Technical University (METU), Turkey, and the University of Phoenix, USA, respectively. He received his PhD from Griffith University, Australia, in Information and Communication Technology, with a focus on underwater & terrestrial communication, tracking and localization. He is currently attached to the Department of Computer Science and Engineering, East West University, Bangladesh, as an Associate Professor. Besides, he has been serving as the Proctor of the same university for the last 5 years.

Short Bio of Dr. Michael Georgiades

Dr Michael Georgiades is an Assistant Professor at Neapolis University of Pafos. He holds a BEng degree in Communications and Radio Engineering from King's College London in 2000 (First Class Honours), an MSc degree in Telecommunications at University College London in 2001, and a PhD in Wireless and Mobile Networks from the University of Surrey in 2008. Past positions held include R&D Manager at Primetel PLC (Cyprus), Research Fellow at the Centre of Communication Systems Research (CCSR), University of Surrey (UK), and Systems Development Engineer at INSIG Ltd (UK). He has been involved in many EU-funded projects in the field of ICT. He is a patent holder and has published in numerous prestigious international journals, conferences, and books, as well as contributed to several IETF standards in the area of ICT. He has received multiple awards, including the Nokia Prize for Research Excellence, Primetel Awards for Excellence and Contribution, as well as EPSRC and British Council studentship awards. Areas of research interest include: Tactile Internet, Mobile Edge Computing, Vehicular Networks, Internet of Things, Edge Intelligence.

Short Bio of Dr. Sanaul Haque

Dr Sanaul Haque is a Senior Behavioral Scientist at Colcrane Behavioral Insights, Helsinki Oy, and as a Post-doctoral Research Scientist at the LUT University, Finland. He holds a PhD in Medicine (Medical Engineering Technology) from the University of Oulu, Finland, in 2020, an MSc degree in Telecommunications from QMUL (Queen Mary, University of London), London, the UK, in 2014, and holds a BSc(Hons) degree in Information Systems from UEL (University of East London), London, the UK, in 2012. He also holds a Certification in Teaching and Learning from South East Technological University (SETU), Ireland, in 2021. During his PhD, he collaborated and worked with King's College London (KCL), the UK, University College Dublin (UCD), Ireland, and his Master's thesis was funded by QMUL and in collaboration with UCL (University College London), the UK. He has been conducting multidisciplinary research activities. His research area covers persuasive technology and gamification for digital solutions. His research aims at how digital interventions can be sustained through citizen behavioural change. Dr Sanaul has worked in many EU projects such as FP7 eDIGIREGION, H2020 CHESS ITN, ENJECT COST program, etc and closely interacted with relevant stakeholders. He has also secured many local Irish and Finnish funds to disseminate his research outputs. He was the co-leader for WP5 of the Smart Campus project (www.smartcampus.fi), an Academy of Finland-funded project (2021-2022).

Short Bio of Dr. Riadul Islam

Dr. Riadul Islam is currently an assistant professor in the Department of Computer Science and Electrical Engineering at the University of Maryland, Baltimore County. In his Ph.D. dissertation work at UCSC, Riadul designed the first current-pulsed flip-flop/register that resulted in the first-ever one-to-many current-mode clock distribution networks for high-performance microprocessors. From 2017 to 2019, he was an Assistant Professor at the University of Michigan, Dearborn, MI, USA. From 2007 to 2009, he worked as a full-time faculty member in the Department of Electrical and Electronic Engineering of the University of Asia Pacific, Dhaka, Bangladesh. He is a member of the ACM, IEEE, IEEE Circuits and Systems (CAS) Society, and IEEE Solid-State Circuits (SSC) Society. He is a member of the Cybersecurity Center for Research, Education, and Outreach at UM-Dearborn. He holds two US patents and several IEEE/ACM/Springer Nature journal and conference publications in TVLSI, TCAS, JETTA, DAC, ISCAS, MWSCAS, ISQED, and ASICON. His current research interests include digital, analog, and mixed-signal CMOS ICs/SOCs for a variety of applications; verification and testing techniques for analog, digital, and mixed-signal ICs; hardware security; CAN network; CAD tools for design and analysis of microprocessors and FPGAs; automobile electronics; and biochips. He is an editorial board member of the Semiconductor Science and Information Devices journal.